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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/886,266	06/22/2001	Hiromasa Funakoshi	0074/009001	5549
22893 7	590 07/28/2005		EXAM	INER
SMITH PATENT OFFICE 1901 PENNSYLVANIA AVENUE N W			VILLECCO, JOHN M	
SUITE 200			ART UNIT	PAPER NUMBER
WASHINGTON, DC 20006			2612	
			DATE MAIL ED: 07/28/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
Office Action Summary		09/886,266	FUNAKOSHI ET AL.				
		Examiner	Art Unit				
		John M. Villecco	2612				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR REF MAILING DATE OF THIS COMMUNICATION nsions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a representation of the provision of t	1.136(a). In no event, however, may a replepty within the statutory minimum of thirty (3 and will apply and will expire SIX (6) MONTH ute, cause the application to become ABAN	y be timely filed  30) days will be considered timely.  S from the mailing date of this communication.  IDONED (35 U.S.C. § 133).				
Status							
1)🛛	Responsive to communication(s) filed on 10	May 2005.					
2a)⊠	<u> </u>						
3)[	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
4)⊠ 5)⊠ 6)⊠ 7)□ 8)□	Claim(s) <u>1-15</u> is/are pending in the application 4a) Of the above claim(s) <u>5-15</u> is/are withdrated Claim(s) <u>2-4</u> is/are allowed.  Claim(s) <u>1</u> is/are rejected.  Claim(s) is/are objected to.  Claim(s) are subject to restriction and	wn from consideration.					
Applicat	ion Papers						
9)[	The specification is objected to by the Exami	ner.					
10)⊠	10)⊠ The drawing(s) filed on <u>22 June 2001</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11)	Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the		• •				
Priority ι	ınder 35 U.S.C. § 119						
12)⊠ a)ĺ	Acknowledgment is made of a claim for foreign All b) Some * c) None of:  1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bure see the attached detailed Office action for a li	nts have been received. nts have been received in App ionty documents have been re au (PCT Rule 17.2(a)).	lication No ceived in this National Stage				
Attachmen	t(s)						
	e of References Cited (PTO-892)	4) Interview Sum					
3) 🔲 Inforr	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/0 r No(s)/Mail Date		lail Date mal Patent Application (PTO-152)				

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#### **DETAILED ACTION**

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# Response to Arguments

- 1. Applicant's arguments filed May 10, 2005 have been fully considered but they are not persuasive.
- Applicant has amended independent claim 1 to show that the output value of the amplifier is maintained while the enable signal is obtained. Applicant then argues that enable single of the applicant's invention and the blanking signal (CBLK) of Suzuki are not the same and thus, the claim language differentiates the invention from the Suzuki reference. However, the examiner disagrees with this assertion. Based on the official translation supplied with this action it is clear that the Suzuki reference can be read on claim 1. More specifically, the clamping pulses (CP1 and /or CP2) are interpreted to be the optical black clock pulses. The blanking pulse (CBLK) can be interpreted as the enable signal (as claimed by the applicant) since the blanking pulse would only operate when output of the solid-state image element stops. Furthermore, when the blanking pulse (CBLK) is activated as shown in Figure 3c, the signal that is passed through the amplifier (16 or 20) is maintained at a constant level. Although the first clamping block (10) was discussed in the previous action. Second clamping block (14) is actually the clamping circuit which provides for maintaining the signal level to a constant value. Since the rejection from the previous office action still reads on the claim, the office action is made final. See paragraphs 0013-0015.
- 3. For the reasons stated above the rejection of claim 1 from the previous office action will be repeated.

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## Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

- 5. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Suzuki et al.

  (Japanese Publ. No. 04-291576 A).
- 6. Regarding *claim 1*, Suzuki discloses a clamping circuit for an electronic endoscope. More specifically, Suzuki discloses a clamping circuit which receives an image signal from a solid-state imaging device. The clamping circuit receives a signal CP1, which indicates an optical black section, and a signal CBLK, which indicates a timing at which an output of the solid-state imaging elements stops. The output of the solid-state imaging device stops at CBLK since this signal represents a blanking signal. The clamping circuit further includes a variable voltage device (VR2), which acts as a target value setting means which sets a target value for output and an amplifier device (17) for amplifying a difference between the target value and an optical black signal. This is fed back to the signal line. The amplifier acts to match a signal level of the optical black section to a constant value by maintaining the target value of the optical black section, while the enable signal (CBLK) is activated. See Figures 1 and 3. Also see paragraphs 0013-0015.

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## Allowable Subject Matter

7. Claims 2-4 are allowed.

8. The following is an examiner's statement of reasons for allowance:

Regarding *claim 2*, the primary reason for allowance is that the prior art fails to teach or reasonably suggest an arrangement of the optical black clamping circuit as specified in claim 2.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

9. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing. date of this final action.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to John M. Villecco whose telephone number is (571) 272-7319. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's 7382 supervisor, Wendy Garber can be reached on (571) 272-7308. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

John M. Villecco July 16, 2005

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